Aidan Venckus

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Education

University of California, Santa Cruz   Graduated: June 2022
Bachelor of Science in Computer Engineering (GPA 3.49)
Concentration in Robotics and Control & Minor in Electrical Engineering

Work Experience

<u>Skydio - Flight Test Engineer Lead</u> - *3D Scan*   June 2022 - Present
- Running and writing smoke, regression, and sanity tests to validate the system.
- Executing flights and logging data to characterize and improve the system.
- Working closely with engineers to identify and solve issues.
- Responsible for validating numerous new feature releases with Jira and Testrails.
- Introduced first mobile workflow automation test script using Appium and Python.

<u>CITRIS - Drone Pilots In Training Program</u> - *Paid Training & Mentorship*   January 2022 - March 2022
- Completed a 10 week program focused on learning to fly various drones, planning autonomous flights, photogrammetry, processing drone data, and earned a FAA part 107 remote pilot license.

<u>UCSC - Computer Systems and Assembly Language</u> - *Tutor & Grader*   September 2019 - March 2021
- Assisted students in learning computer system fundamentals including computer logic, assembly language, data path, various data types, git, multimedia logic, ascii, and much more.
- Guided students through the design, development, and implementation of assembly language in class assignments.

<u>UCSC - Embedded Systems and C Programming</u> - *Tutor & Grader*   January 2021 - March 2021
- Aided in teaching an introduction to the C programming language as a means for controlling embedded systems, In addition to tutoring coursework, helped students develop and debug complex C programs.

Skills

C / C++	Python	Assembly Language
Matlab	Algorithms/Data Structures	FPGA using Verilog
Test Engineering	Soldering / Circuitry	Solidworks / Fusion360
Microcontrollers	3D printing	Git, Bash, Command Line

Key Coursework

- <u>Capstone: Mechatronics</u> *(A)* - Combination of software engineering and electrical engineering to build autonomous wheel driven bots to complete randomized fields with focuses in C, sensors, filtering, event driven programming, CAD design, motors, and power systems.
- <u>Microprocessor System Design</u> *(A)* - Design and use of microprocessor and microcontroller architecture, bus and memory organization, interrupts, peripheral devices, etc in C on PSoC.
- <u>Logic Design</u> *(A-)* - Boolean algebra, logic minimization, finite-state machines, sequential circuits, introduction to system level design, etc in Verilog on FPGA.
- <u>Introduction to Algorithm and Data Structures</u> *(A)* - Intro to abstract data types and algorithms; linked lists, stacks, queues, hash tables, trees, heaps, and graphs, etc in C.

Leadership Positions and Activities

Member of the UCSC CubeSat Club   Winter 2021 - Spring 2021
- My responsibilities include working with the On-Board-Computer subteam.

Member of the UCSC Amateur Radio Club   Spring 2019 - Spring 2020
- Technician license for HAM radio (Callsign: "KN6CFM")